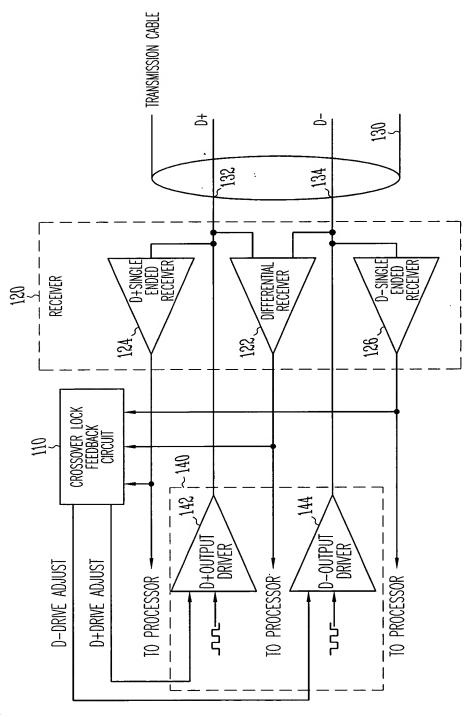
TITLE: CROSS-OVER VOLTAGE LOCK FOR DIFFERENTIAL OUTPUT DRIVERS

INVENTORS NAME: James D. Patterson SERIAL NO.: 10/612290

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Pig. 1

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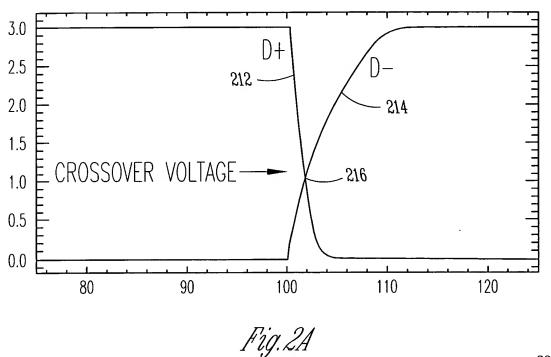


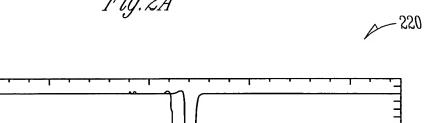
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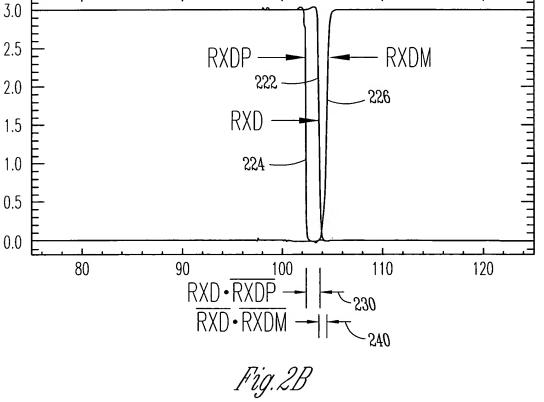
INVENTORS NAME: James D. Patterson SERIAL NO.: 10/612290

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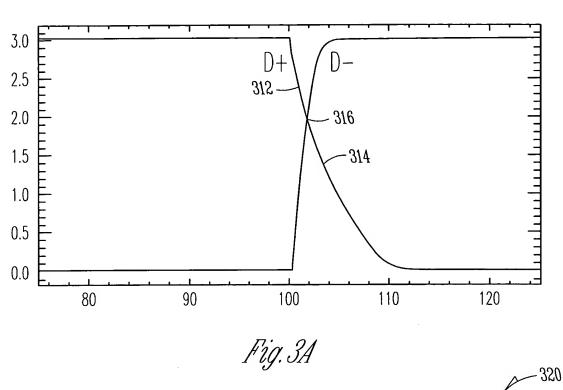


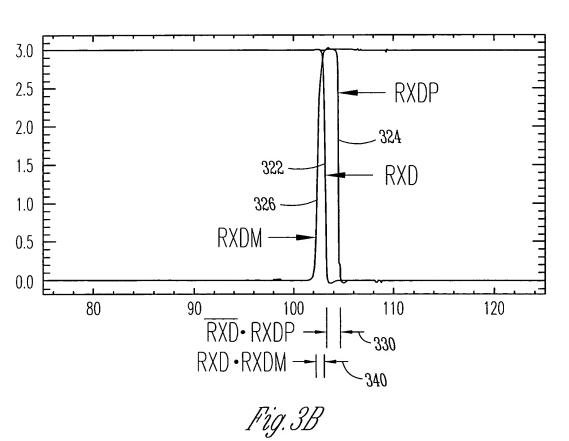


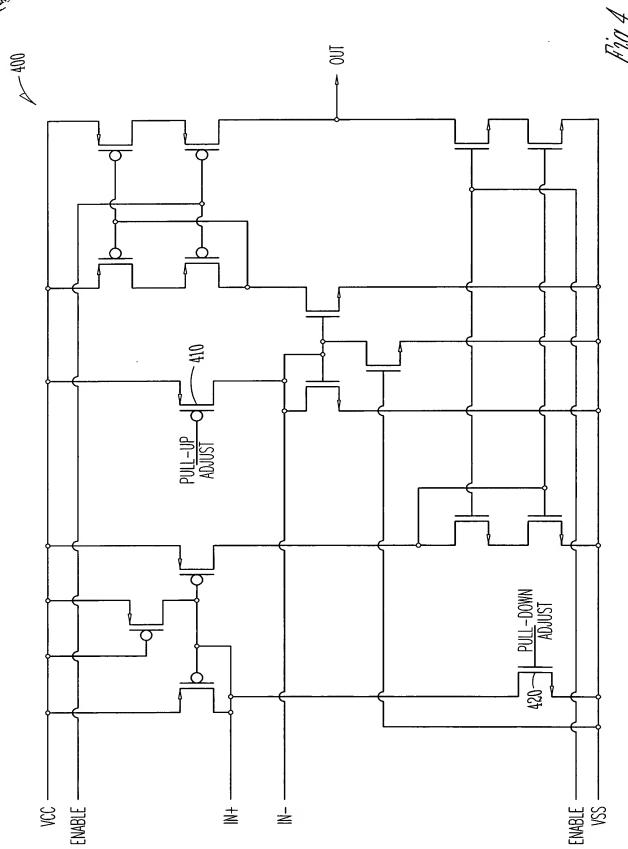


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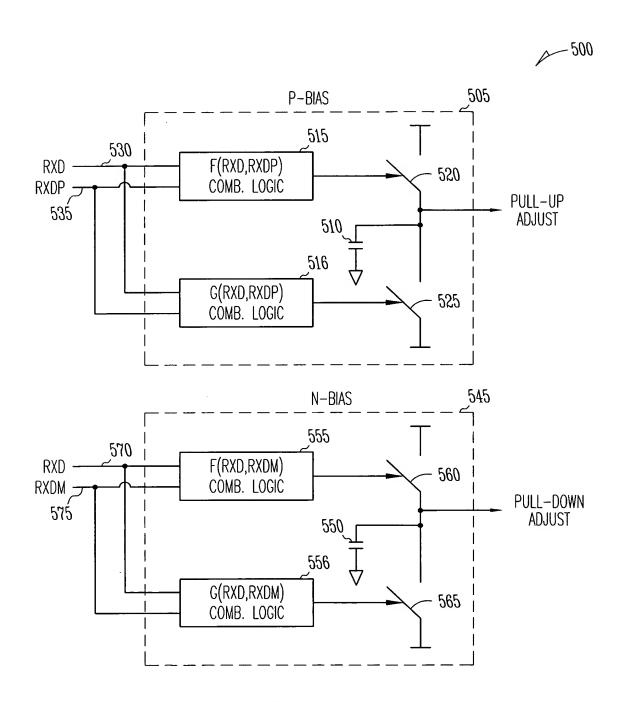
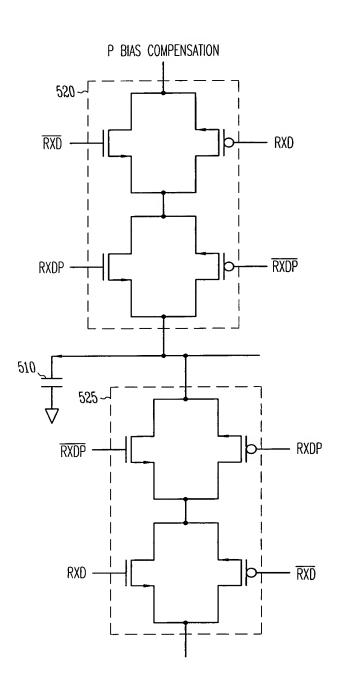


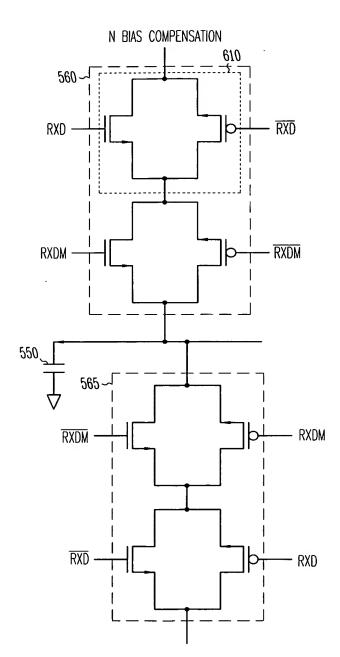
Fig. 5

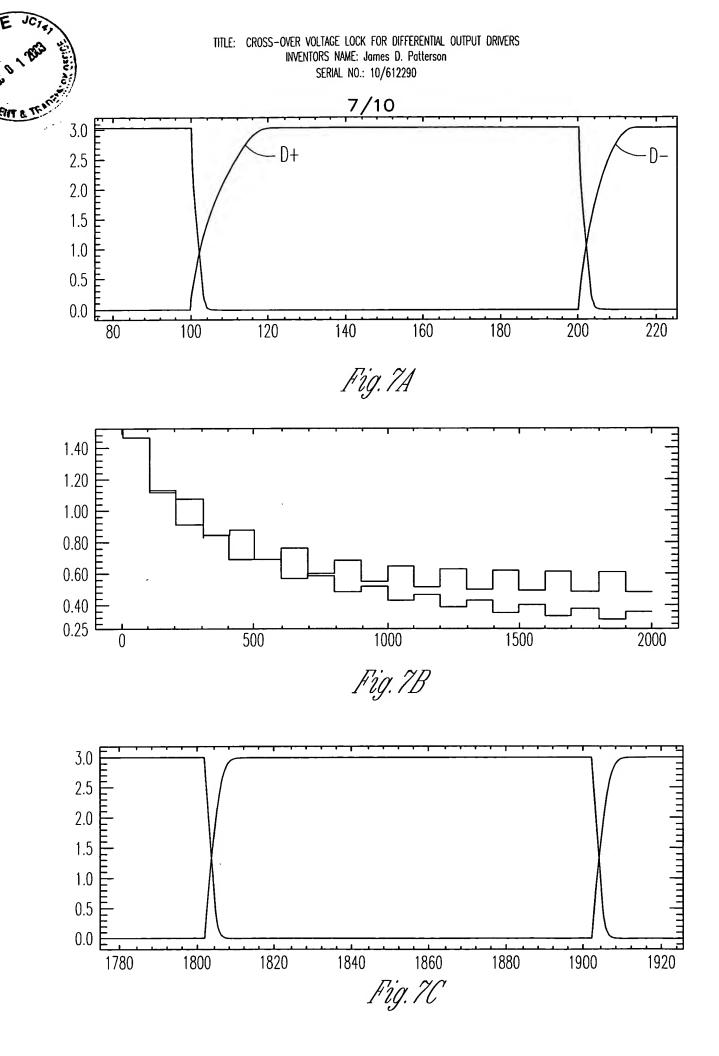


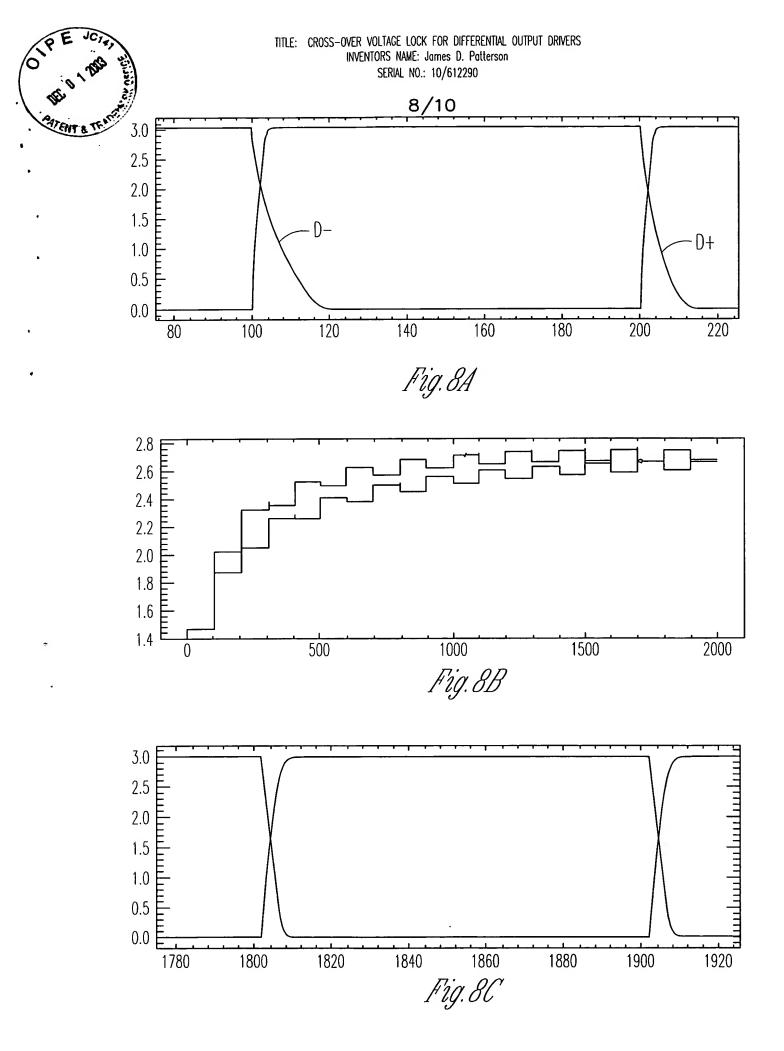
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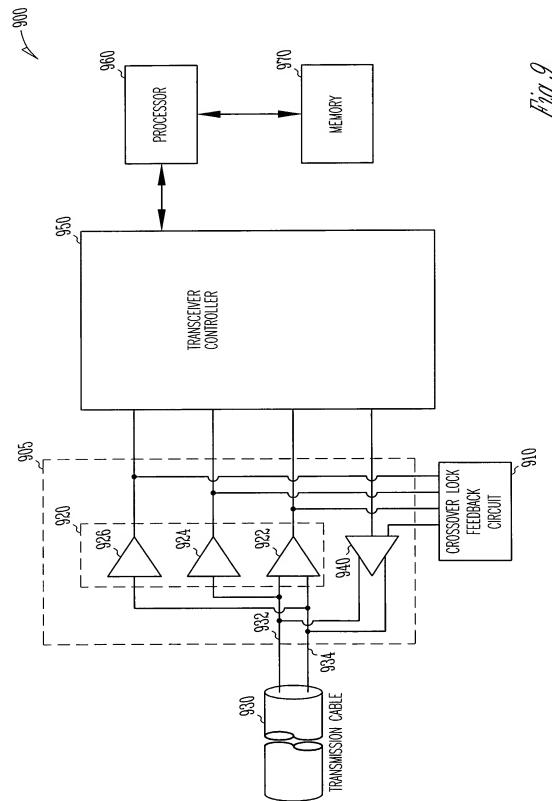








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MEASURING A DIFFERENCE BETWEEN A VOLTAGE AT
WHICH OUTPUT VOLTAGES OF FIRST AND SECOND
DIFFERENTIAL DRIVERS OF A DIFFERENTIAL SIGNAL
TRANSCEIVER CROSS-OVER AND A POINT SUBSTANTIALLY
EQUIDISTANT BETWEEN MAXIMUM AND MINIMUM
OUTPUT VOLTAGES

1050

1010

PROVIDING A CORRECTING BIAS VOLTAGE
PROPORTIONAL TO A DIFFERENCE BETWEEN THE
CROSS-OVER VOLTAGE AND THE EQUIDISTANT
VOLTAGE

1030

APPLYING THE CORRECTING BIAS VOLTAGE TO THE DIFFERENTIAL DRIVERS TO VARY THE POINT WHERE THE FIRST AND SECOND VOLTAGES CROSS-OVER

Fig. 10